

Philips

Diode BYG70G

Datasheet

Silicon Diode

BYG70G

400V/390mA

DATASHEET

OEM – Philips

Source: Philips Databook 1999

**Fast soft-recovery
controlled avalanche rectifiers**
BYG70 series**FEATURES**

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- UL 94V-O classified plastic package
- Shipped in 12 mm embossed tape.

DESCRIPTION

DO-214AC surface mountable package with glass passivated chip.

The well-defined void-free case is of a transfer-moulded thermo-setting plastic.

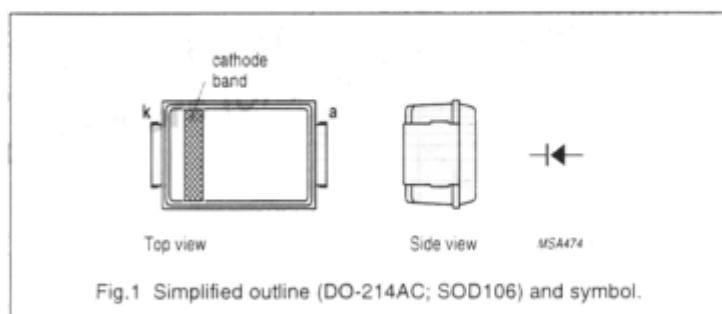


Fig.1 Simplified outline (DO-214AC; SOD106) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{RRM}	repetitive peak reverse voltage		–	200	V
V _R	continuous reverse voltage		–	200	V
I _{F(AV)}	average forward current	averaged over any 20 ms period; T _{tp} = 100 °C; see Fig.2	–	1.00	A
		averaged over any 20 ms period; Al ₂ O ₃ PCB mounting (see Fig.7); T _{amb} = 60 °C; see Fig.3	–	0.53	A
		averaged over any 20 ms period; epoxy PCB mounting (see Fig.7); T _{amb} = 60 °C; see Fig.3	–	0.39	A
I _{FSM}	non-repetitive peak forward current	t = 10 ms half sine wave; T _j = T _{j max} prior to surge; V _R = V _{RRMmax}	–	20	A
E _{RSM}	non-repetitive peak reverse avalanche energy	L = 120 mH; T _j = T _{j max} prior to surge; inductive load switched off	–	10	mJ
T _{stg}	storage temperature		–65	+175	°C
T _j	junction temperature	see Fig.4	–65	+175	°C

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ELECTRICAL CHARACTERISTICS

$T = 25^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_F	forward voltage	$I_F = 1 \text{ A}; T_j = T_{j\max}$; see Fig.5	–	–	2.1	V
		$I_F = 1 \text{ A}$; see Fig.5	–	–	3.6	V
$V_{(BR)R}$	reverse avalanche breakdown voltage BYG70D BYG70G BYG70J	$I_R = 0.1 \text{ mA}$	300	–	–	V
I_R	reverse current	$V_R = V_{RRM\max}$; see Fig.6	–	–	5	μA
		$V_R = V_{RRM\max}; T_j = 165^\circ\text{C}$; see Fig.6	–	–	100	μA
t_{rr}	reverse recovery time	when switched from $I_F = 0.5 \text{ A}$ to $I_R = 1 \text{ A}$; measured at $I_R = 0.25 \text{ A}$; see Fig.8	–	–	30	ns
C_d	diode capacitance	$V_R = 0 \text{ V}; f = 1 \text{ MHz}$	–	30	–	pF

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th,j-tp}$	thermal resistance from junction to tie-point		25	K/W
$R_{th,j-a}$	thermal resistance from junction to ambient	note 1	100	K/W
		note 2	150	K/W

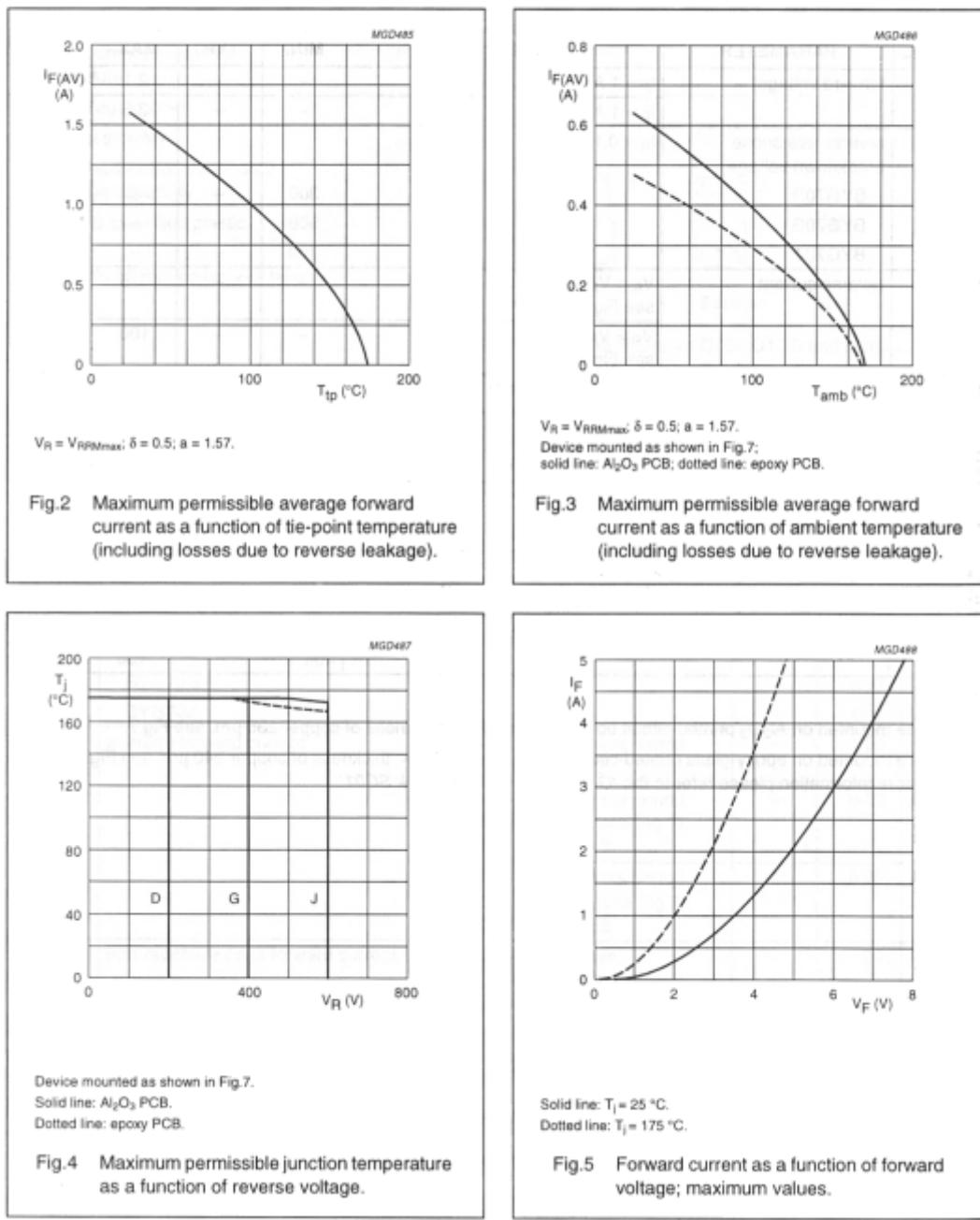
Notes

1. Device mounted on Al_2O_3 printed-circuit board, 0.7 mm thick; thickness of copper $\geq 35 \mu\text{m}$, see Fig.7.
2. Device mounted on epoxy-glass printed-circuit board, 1.5 mm thick; thickness of copper $\geq 40 \mu\text{m}$, see Fig.7.
For more information please refer to the 'General Part of Handbook SC01'.

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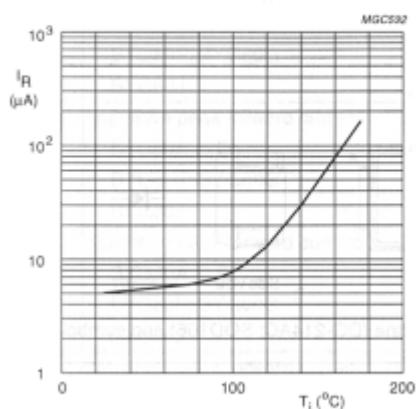
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GRAPHICAL DATA



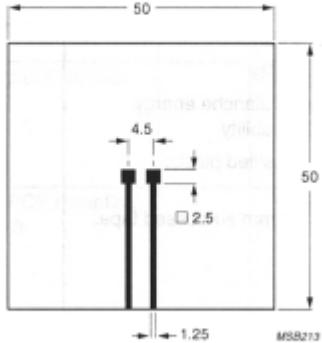
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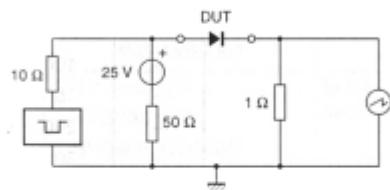
$V_R = V_{RWMmax}$.

Fig.6 Reverse current as a function of junction temperature; maximum values.



Dimensions in mm.
Material: Al_2O_3 or epoxy-glass.

Fig.7 Printed-circuit board for surface mounting.



Input impedance oscilloscope: $1 \text{ M}\Omega$, 22 pF ; $t_r \leq 7 \text{ ns}$.
Source impedance: 50Ω ; $t_r \leq 15 \text{ ns}$.

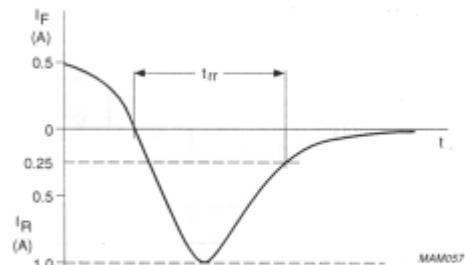


Fig.8 Test circuit and reverse recovery time waveform and definition.