

Philips

Diode PBYR2100CT

Datasheet

# Schottky Dual Diode

## **PBYR2100CT**

100V / 2A

# **DATASHEET**

OEM – Philips

Source: Philips Databook 1999

**Schottky barrier double diodes****PBYR2100CT series****FEATURES**

- Low switching losses
- High breakdown voltage
- Fast recovery time
- Guard ring protected
- Plastic SMD package.

**PINNING**

PIN	DESCRIPTION
1	anode ( $a_1$ )
2	common cathode
3	anode ( $a_2$ )
4	common cathode

**MARKING**

TYPE NUMBER	MARKING CODE
PBYR280CT	BYR28
PBYR290CT	BYR29
PBYR2100CT	BYR210

**APPLICATIONS**

- Low power, switched-mode power supplies
- Rectification
- Polarity protection.

**DESCRIPTION**

The PBYR2100CT series consists of Schottky barrier double diodes, fabricated in planar technology, and encapsulated in SOT223 plastic SMD packages.

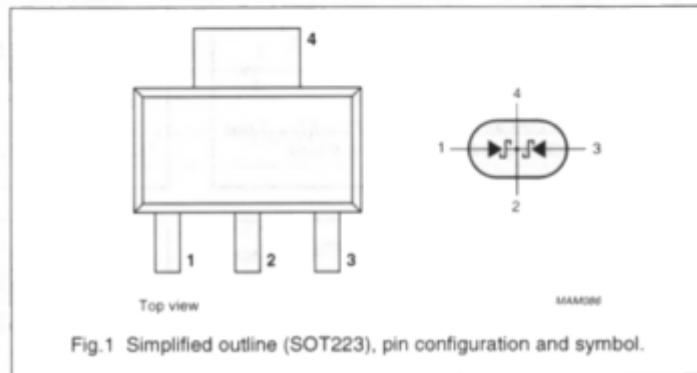


Fig.1 Simplified outline (SOT223), pin configuration and symbol.

## Schottky barrier double diodes

## PBYR2100CT series

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
<b>Per diode</b>					
$V_R$	continuous reverse voltage				V
$V_{R(A)}$	PBYR280CT	$T_{amb} = 25^{\circ}\text{C}$ ; $I_F = 1\text{ A}$	-	80	V
$V_{R(B)}$	PBYR290CT	$T_{amb} = 25^{\circ}\text{C}$ ; $I_F = 1\text{ A}$	-	90	V
$V_{R(C)}$	PBYR2100CT	$T_{amb} = 25^{\circ}\text{C}$ ; $I_F = 1\text{ A}$	-	100	V
$V_{RRM}$	repetitive peak reverse voltage				V
	PBYR280CT	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	80	V
	PBYR290CT	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	90	V
	PBYR2100CT	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	100	V
$V_{RWM}$	crest working reverse voltage				V
	PBYR280CT	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	80	V
	PBYR290CT	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	90	V
	PBYR2100CT	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	100	V
$I_{F(AV)}$	average forward current	$T_{amb} = 85^{\circ}\text{C}$ ; see Fig.2; $R_{th(j-a)} = 70\text{ K/W}$ ; note 1; $V_{R(equiv)} = 0.2\text{ V}$ ; note 2	-	1	A
$I_{FSM}$	non-repetitive peak forward current	$t = 8.3\text{ ms}$ half sine wave; JEDEC method	-	10	A
$I_{RSM}$	non-repetitive peak reverse current	$t_p = 100\text{ }\mu\text{s}$	-	0.5	A
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature		-65	+150	°C
$T_{amb}$	operating ambient temperature		-	85	°C

## Notes

- Refer to SOT223 standard mounting conditions.
- For Schottky barrier diodes thermal run-away has to be considered, as in some applications, the reverse power losses  $P_R$  are a significant part of the total power losses. Nomograms for determination of the reverse power losses  $P_R$  and  $I_{F(AV)}$  rating will be available on request.

## Schottky barrier double diodes

## PBYR2100CT series

## ELECTRICAL CHARACTERISTICS

 $T_{amb} = 25^{\circ}\text{C}$ ; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Per diode</b>						
$V_F$	forward voltage	see Fig.3 $I_F = 1 \text{ A}$ ; note 1 $I_F = 1 \text{ A}; T_j = 100^{\circ}\text{C}$ ; note 1	-	-	790	mV
$I_R$	reverse current	$V_R = V_{RRMmax}$ ; note 1; see Fig.4	-	-	0.5	mA
		$V_R = V_{RRMmax}$ ; $T_j = 100^{\circ}\text{C}$ ; note 1; see Fig.4	-	-	5	mA
$C_d$	diode capacitance	$V_R = 4 \text{ V}$ ; $f = 1 \text{ MHz}$ ; see Fig.5	-	-	100	pF

## Note

1. Pulsed test:  $t_p = 300 \mu\text{s}$ ;  $\delta = 0.02$ .

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th,j-a}$	thermal resistance from junction to ambient	note 1	70	K/W

## Note

1. Refer to SOT223 standard mounting conditions.

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## GRAPHICAL DATA

